

For example, as shown in Figure 3, the first layer 11 is disposed on the piezoelectric substrate 13 and the internal conductive contacts 111 are disposed on a face 14 of the piezoelectric substrate 13 so that the first layer 11 is in direct contact and completely surrounds the internal conductive contacts 111. Further, the first layer 11 is disposed on the piezoelectric substrate 13 so that it completely surrounds each of the conductive via holes (Figure 3).

The outstanding Office Action states Onishi et al ('142), Tsuji et al or Onishi et al ('368) in view of Takoshima disclose the claimed invention. However, Applicants note Onishi et al ('142) show in Figure 16 electrode pads 3a and 3b that are not completely surrounded by a resin layer 7. Further, Tsuji et al disclose in Figure 1(a) electrode pads 15 that are not completely surrounded by an insulating resin layer 13, and Onishi et al ('368) show in Figure 1 terminals 3 that are not completely surrounded by a resin layer 7. Takoshima discloses in Figure 3 terminal sections 14 and 25 connected by a solder that is not shown in the drawings.<sup>1</sup> However, Takoshima does not teach or suggest a first layer in direct contact and completely surrounding the terminal sections 14 and 25.

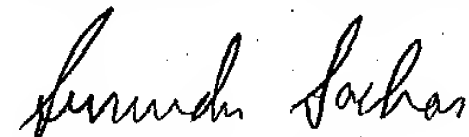
Thus, the combination of Onishi et al ('142), Tsuji et al or Onishi et al ('368) in view of Takoshima does not render obvious the invention of Claim 1. Accordingly, it is respectfully submitted independent Claim 1 and each of the claims depending therefrom are allowable.

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<sup>1</sup>Takoshima, column 3, lines 34-36.

Consequently, in light of the above discussion and in view of the present amendment, the present application is believed to be in condition for allowance and an early and favorable action to that effect is respectfully requested.

Respectfully submitted,  
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IN THE CLAIMS

Please amend Claim 1 as follows:

1. (Three time Amended) A surface acoustic wave component comprising:  
a piezoelectric substrate having a surface including at least one active surface;  
at least two internal conductive contacts disposed on a face of the piezoelectric substrate;  
a first layer located on the face of the piezoelectric substrate and having a hollowed out portion at least to a level of the at least one active surface;  
a printed circuit board covering the entire first layer and the hollowed out portion and having an opposing surface provided opposite the face of the piezoelectric substrate with the first layer disposed between said opposing surface and said face, said opposing surface having an area equal to an area of said face of said piezoelectric substrate, said printed circuit board further having external conductive contacts; and  
conductive via holes going through the first layer and the printed circuit board and connecting the internal and external conductive contacts,  
wherein the first layer is in direct contact and completely surrounds each of the conductive via holes and each of the internal conductive contacts.